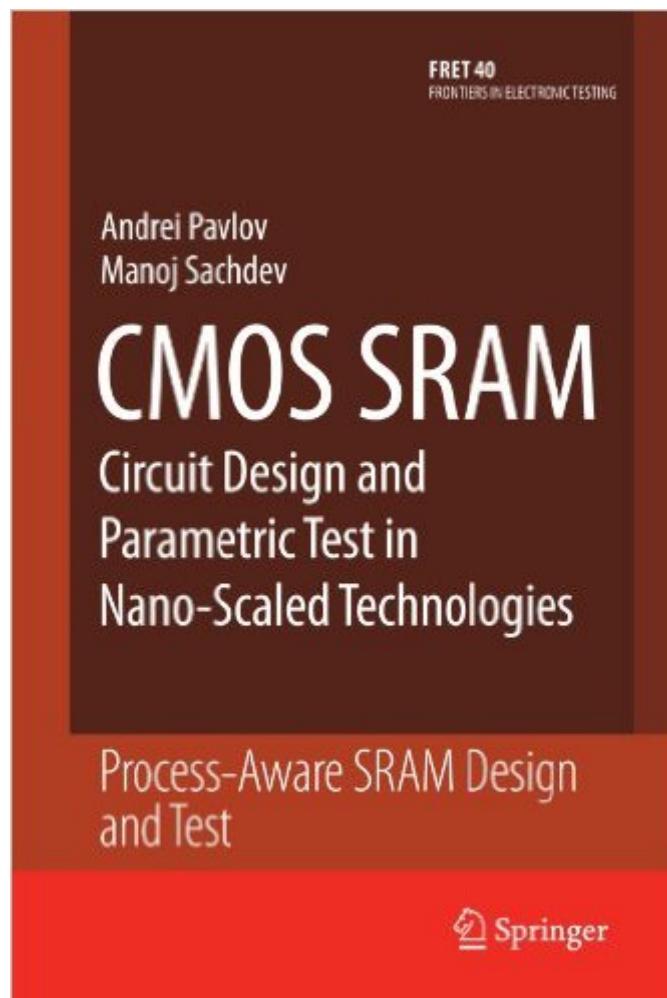


The book was found

# **CMOS SRAM Circuit Design And Parametric Test In Nano-Scaled Technologies: Process-Aware SRAM Design And Test (Frontiers In Electronic Testing)**





## Synopsis

The monograph will be dedicated to SRAM (memory) design and test issues in nano-scaled technologies by adapting the cell design and chip design considerations to the growing process variations with associated test issues. Purpose: provide process-aware solutions for SRAM design and test challenges.

## Book Information

Series: Frontiers in Electronic Testing (Book 40)

Paperback: 194 pages

Publisher: Springer; Softcover reprint of hardcover 1st ed. 2008 edition (February 19, 2010)

Language: English

ISBN-10: 904817855X

ISBN-13: 978-9048178551

Product Dimensions: 6 x 0.5 x 9 inches

Shipping Weight: 12 ounces (View shipping rates and policies)

Average Customer Review: Be the first to review this item

Best Sellers Rank: #5,933,541 in Books (See Top 100 in Books) #83 in Books > Computers & Technology > Programming > Algorithms > Memory Management #1358 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics > Semiconductors #1639 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics > Microelectronics

[Download to continue reading...](#)

CMOS SRAM Circuit Design and Parametric Test in Nano-Scaled Technologies: Process-Aware SRAM Design and Test (Frontiers in Electronic Testing) Nano-CMOS Circuit and Physical Design Delay Fault Testing for VLSI Circuits (Frontiers in Electronic Testing) Nano-CMOS Gate Dielectric Engineering Winter Circuit (Show Circuit Series -- Book 2) (The Show Circuit) Circuit Engineering: The Beginner's Guide to Electronic Circuits, Semi-Conductors, Circuit Boards, and Basic Electronics Electronic Circuits: The Definitive Guide to Circuit Boards, Testing Circuits and Electricity Principles Enzyme Nanoparticles: Preparation, Characterisation, Properties and Applications (Micro and Nano Technologies) CMOS Analog Circuit Design (The Oxford Series in Electrical and Computer Engineering) CMOS Circuit Design, Layout, and Simulation, 3rd Edition (IEEE Press Series on Microelectronic Systems) CMOS Analog Circuit Design Designing Dynamic Circuit Response (Analog Circuit Design) Engineering Design and Creo Parametric 3.0 ENGR 100: Introduction to

Computer Aided Design - AutoCAD 2015 & Creo Parametric 3.0 Variation-Aware Design of Custom Integrated Circuits: A Hands-on Field Guide Summer Circuit (Show Circuit Series -- Book 1) 2015 Federal Circuit Yearbook: Patent Law Developments in the Federal Circuit Parametric Modeling with NX 9 Creo Parametric 3.0 Black Book Transform Circuit Analysis for Engineering and Technology (Electronic Technology)

[Dmca](#)